Appl. No. 10/604,897
Amdt. Dated August 10, 2005
Reply to Office action of June 08, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

5   Claim 1 (Currently Amended): An inverter for providing AC voltages having two different frequencies to a lamp circuit, the inverter comprising:

a first switch for passing a DC voltage;

a first oscillating circuit coupled to the first switch for receiving the DC voltage and generating a first AC voltage having a first frequency;

10   a second oscillating circuit coupled to the first switch for receiving the DC voltage and generating a second AC voltage having a second frequency;

a second switch coupled between a power supply and the first switch for passing the first DC voltage or a second DC voltage provided by the power supply to the first switch as the DC voltage; and

15   a transformer coupled to the first oscillating circuit and the second oscillator for transforming the first AC voltage provided by the first oscillating circuit or the second AC voltage provided by the second oscillator into a third AC voltage and passing the third AC voltage to the lamp circuit;

wherein the first switch selectively passes the DC voltage to the first oscillating
20   circuit or the second oscillating circuit.

Claim 2 (Currently Amended): The inverter of claim 1, wherein the first switch is coupled to [[a]] the power supply for receiving the DC voltage provided by the power supply.

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Claim 3 (Cancelled)

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Claim 4 (Original): The inverter of claim 1, wherein the first oscillating circuit comprises:

a first capacitor coupled between a first node and a second node;

a first resistor coupled between a third node and a fourth node;

5   a second resistor coupled between the third node and a fifth node;

a first transistor having a first end coupled to the first node, a second end coupled to the fourth node, and a third end coupled to ground; and

a second transistor having a first end coupled to the second node, a second end coupled to the fifth node, and a third end coupled to ground.

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Claim 5 (Original): The inverter of claim 4, wherein the transformer comprises:

a first coil coupled between the first node and the third node;

a second coil coupled between the third node and the second node;

a third coil coupled between the fifth node and the fourth node; and

15   a seventh coil coupled between a first end and a second end of the lamp circuit.

Claim 6 (Original): The inverter of claim 1, wherein the second oscillating circuit comprises:

a second capacitor coupled between a sixth node and a seventh node;

20   a third resistor coupled between an eighth node and a ninth node;

a fourth resistor coupled between the eighth node and a tenth node;

a third transistor having a first end coupled to the sixth node, a second end coupled to the ninth node, and a third end coupled to ground; and

a fourth transistor having a first end coupled to the seventh node, a second end

25   coupled to the tenth node, and a third end coupled to ground.

Claim 7 (Original): The inverter of claim 6, wherein the transformer comprises:

a fourth coil coupled between the sixth node and the eighth node;

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a fifth coil coupled between the eighth node and the seventh node;

a sixth coil coupled between the tenth node and the ninth node; and

a seventh coil coupled between a first end and a second end of the lamp circuit.

5   Claim 8 (Original): The inverter of claim 1, wherein the inverter is used in a scanner, a
      multi-function peripheral, or a fax machine.

      Claim 9 (Cancelled)

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